

Fig. 1

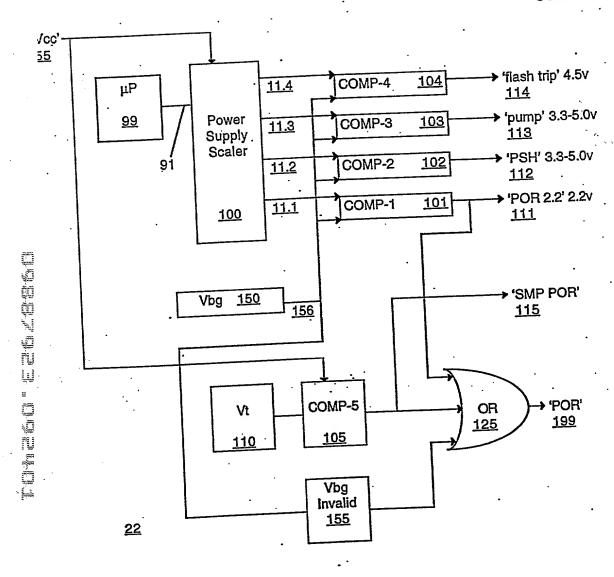


Fig. 2

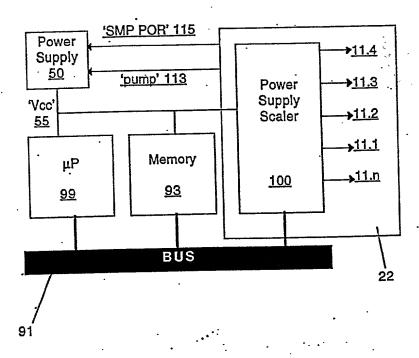


Fig. 3

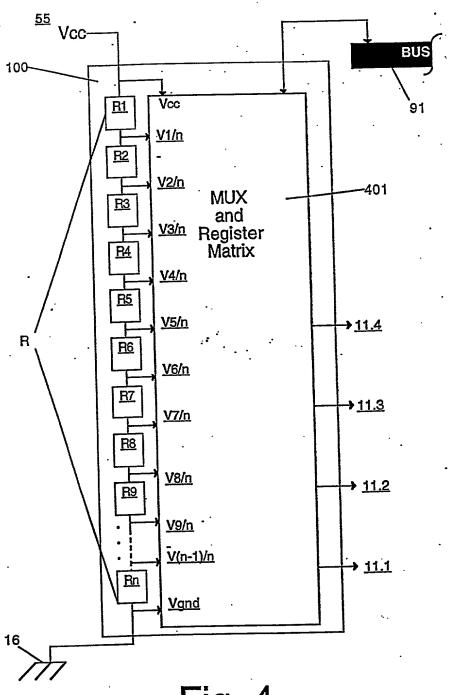


Fig. 4

CONFIDENTIAL Start <u>500</u> Apply Power to System <u>501</u> Assert POR <u>502</u> Initiate Vt Bootstrap <u>503</u> Activate SMP <u>504</u> Sense Vcc <u>505</u> Sense Vbg 507 Pump Up Vcc <u>506</u> Vcc ≥ Vbg X sf? 508 Yes **Deassert POR**

Fig. 5

<u>509</u>

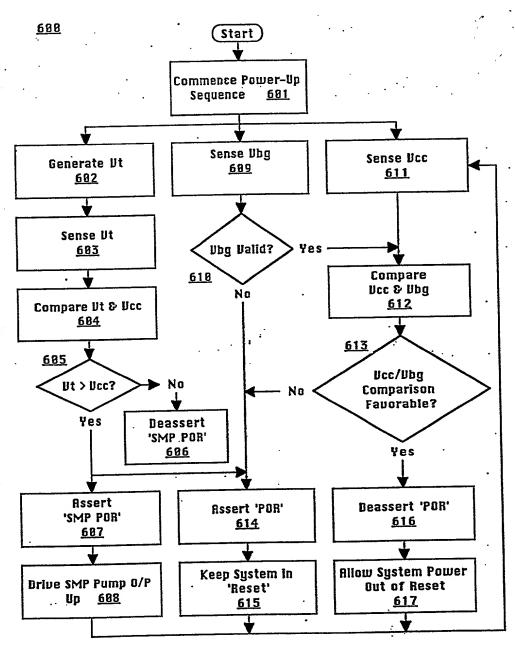
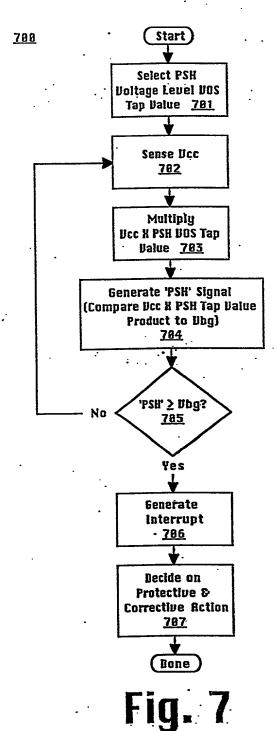


Fig. 6



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